

Fig. 1A

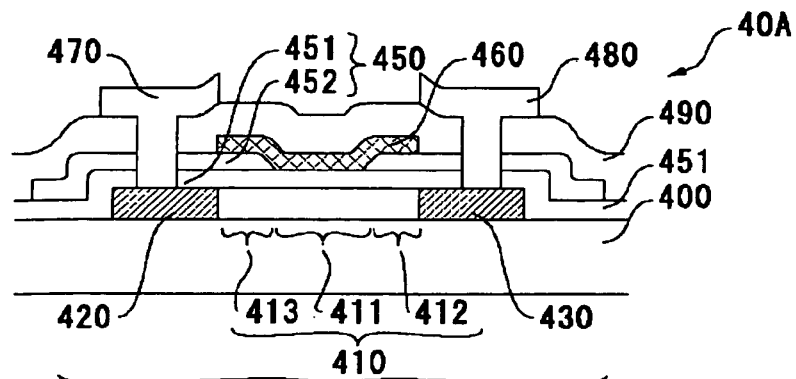


Fig. 1B

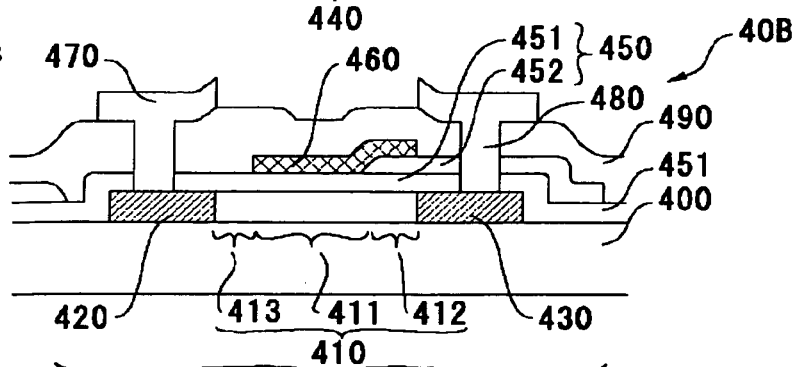


Fig. 1C

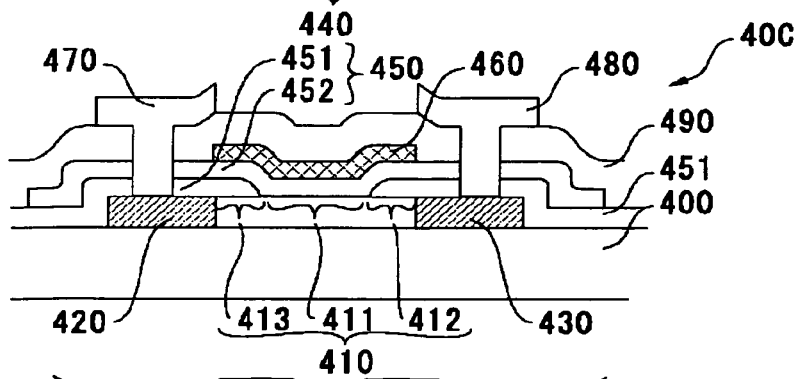


Fig. 1D

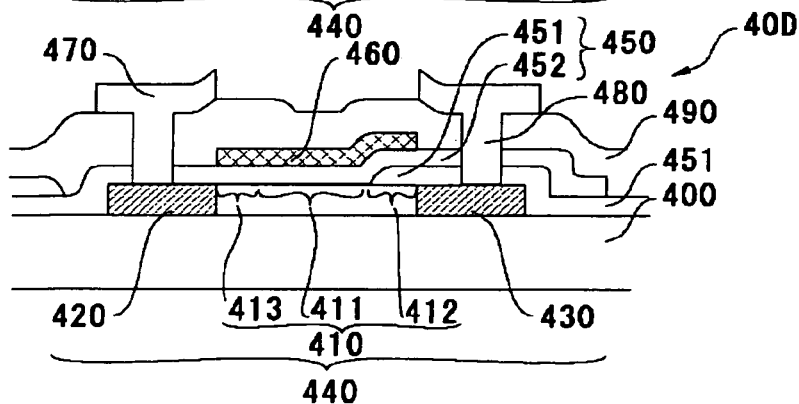
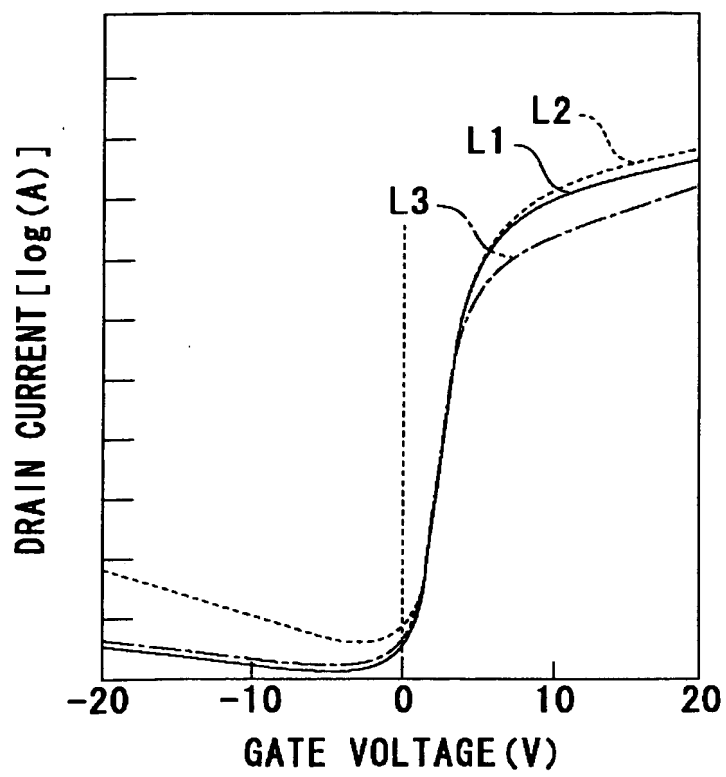


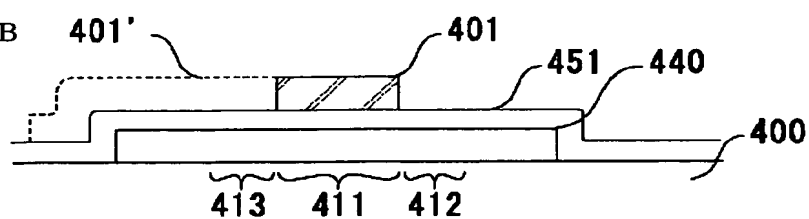
Fig. 2



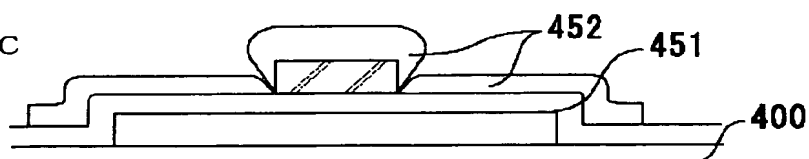
F i g . 3 A



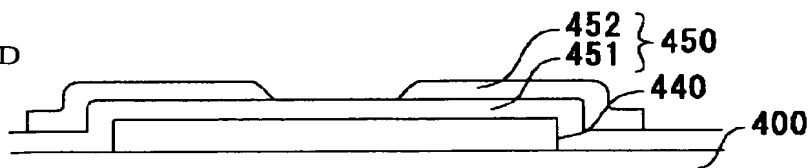
F i g . 3 B



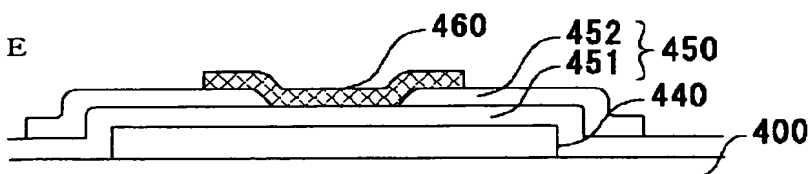
F i g . 3 C



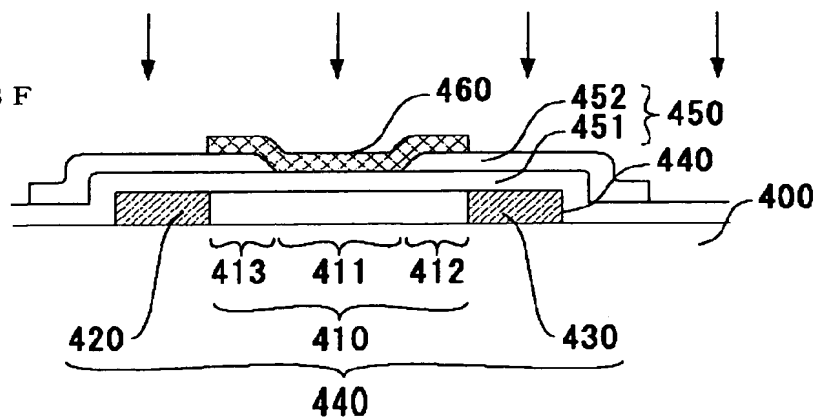
F i g . 3 D



F i g . 3 E



F i g . 3 F



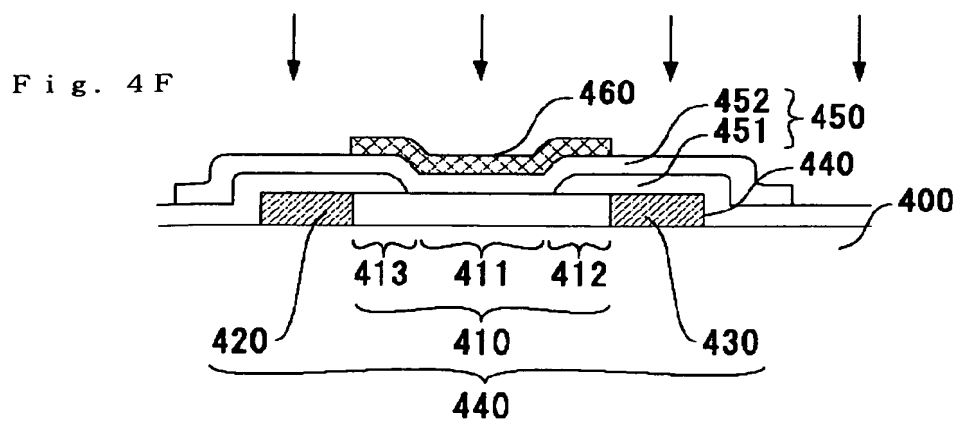
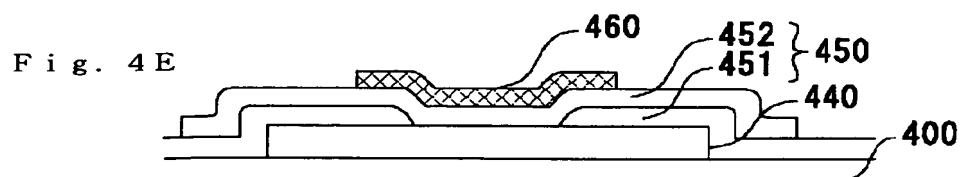
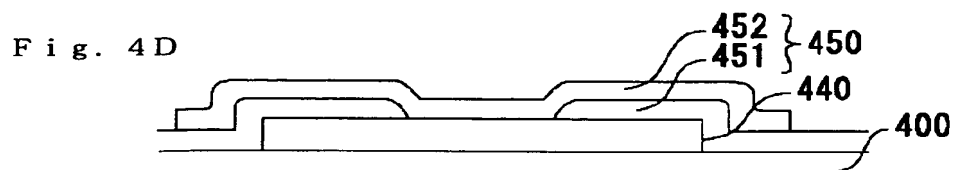
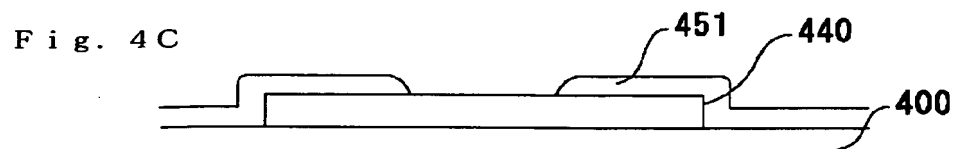
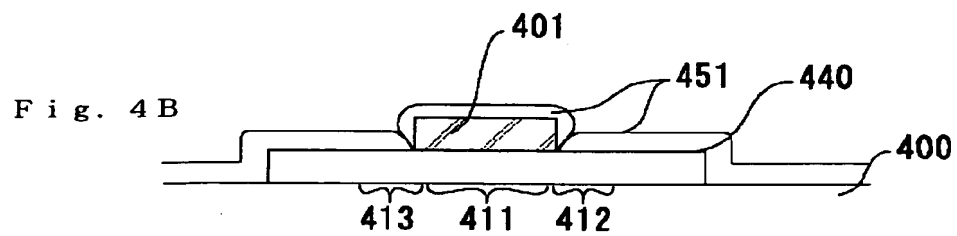
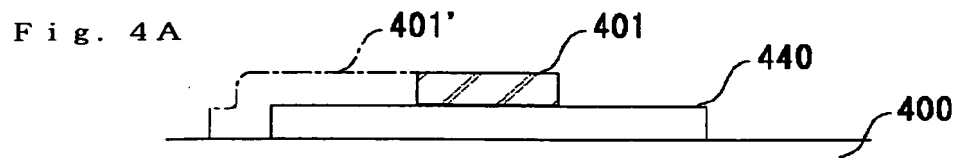


Fig. 5A

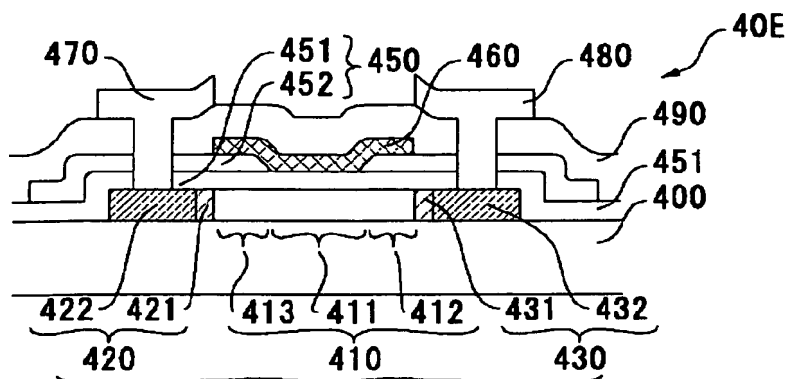


Fig. 5B

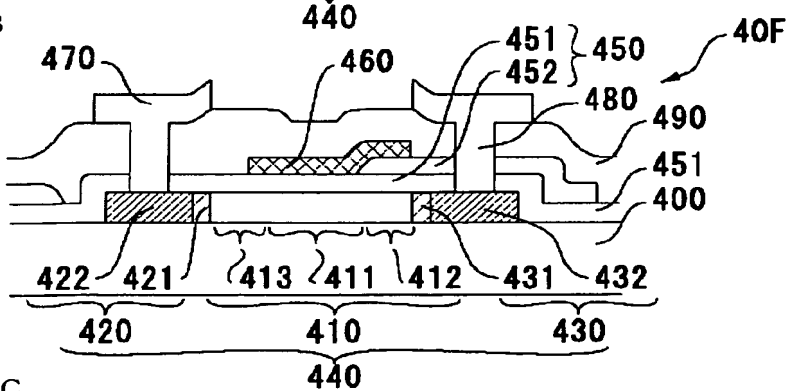


Fig. 5C

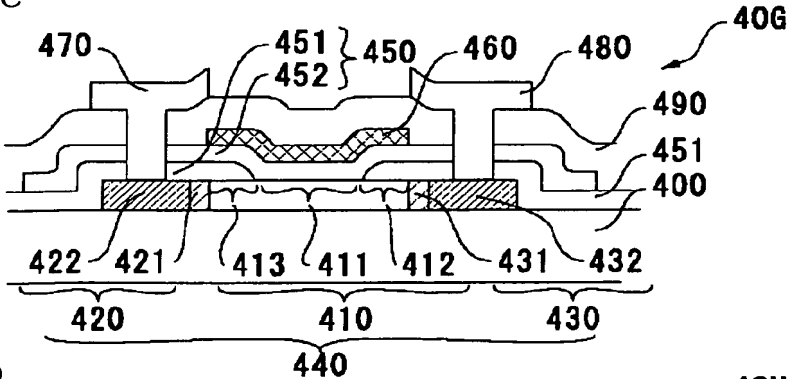


Fig. 5D

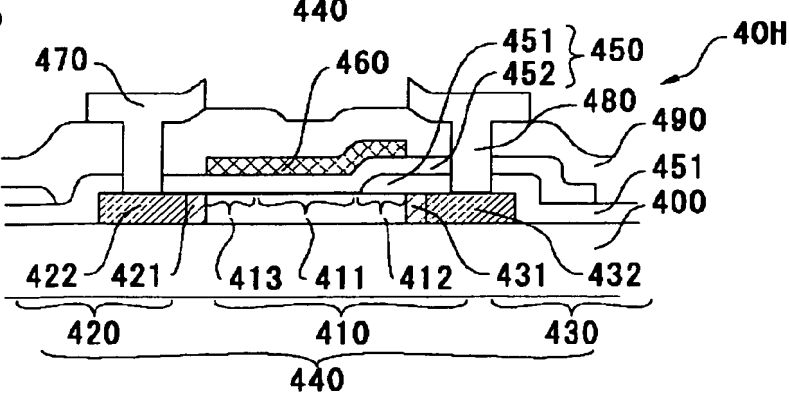


Fig. 6

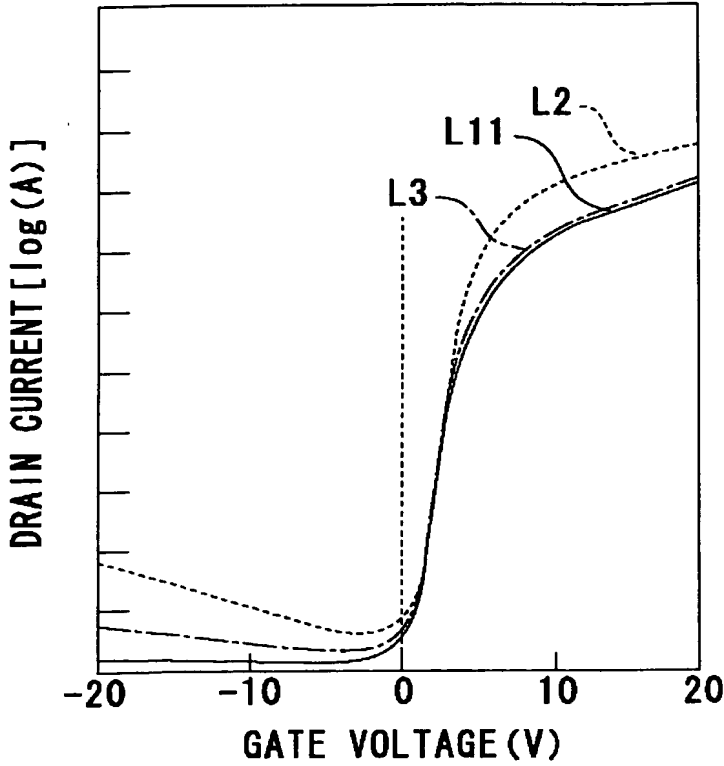


Fig. 7A

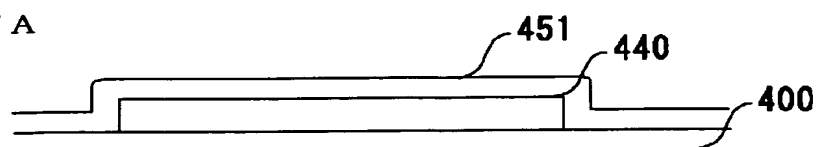


Fig. 7B

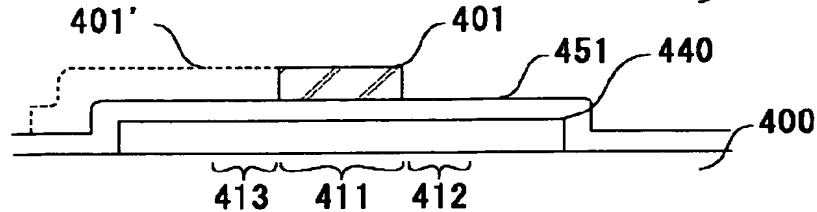


Fig. 7C

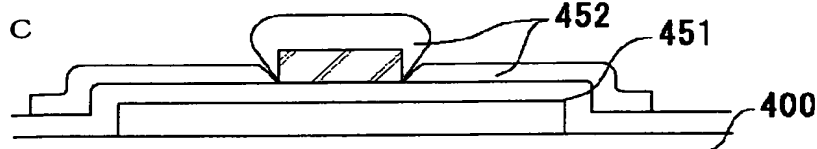


Fig. 7D

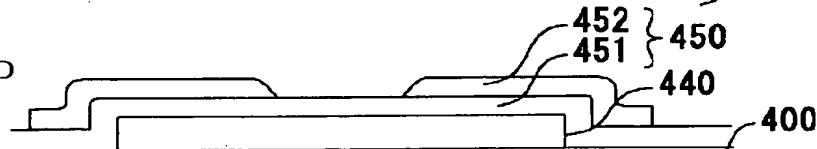


Fig. 7E

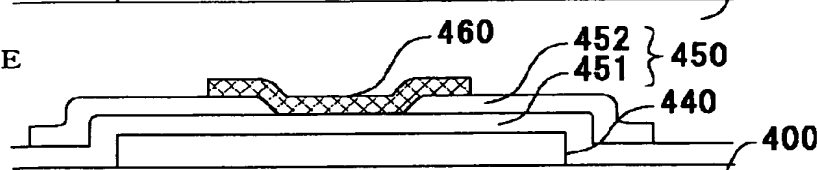


Fig. 7F

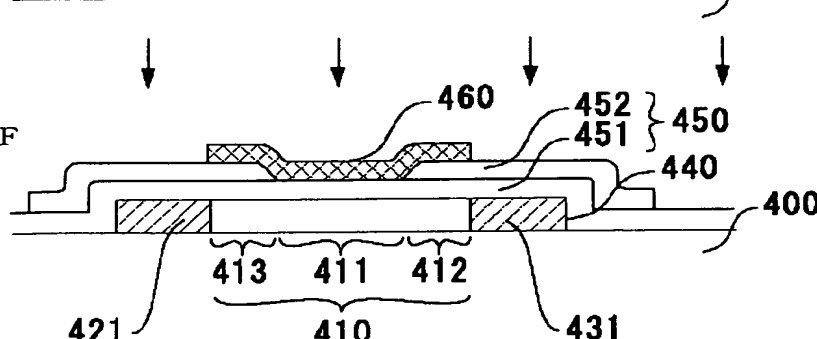


Fig. 7G

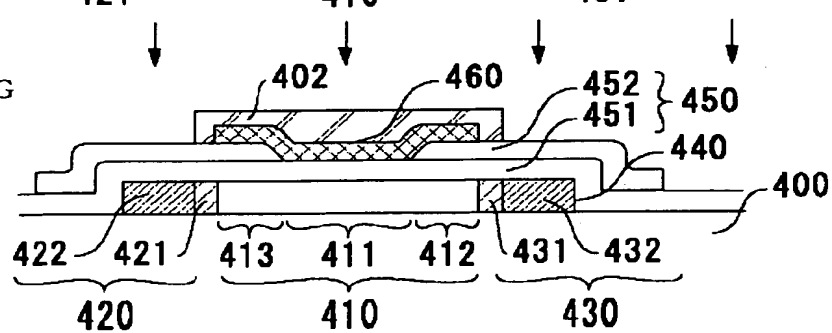


Fig. 8A

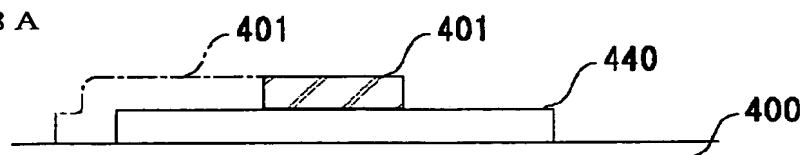


Fig. 8B

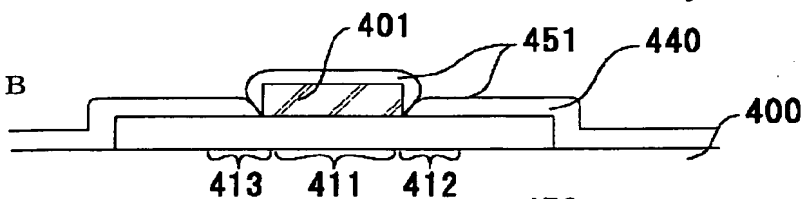


Fig. 8C

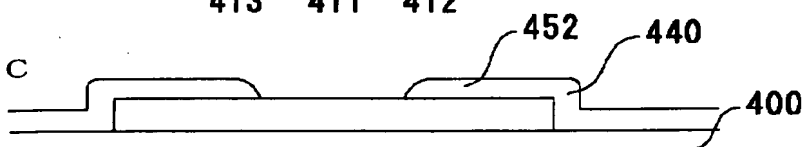


Fig. 8D

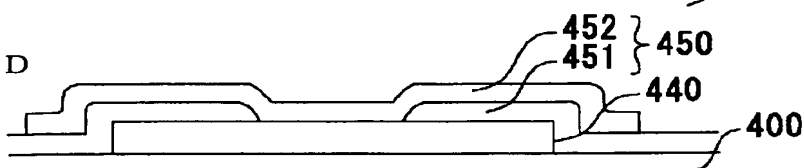


Fig. 8E

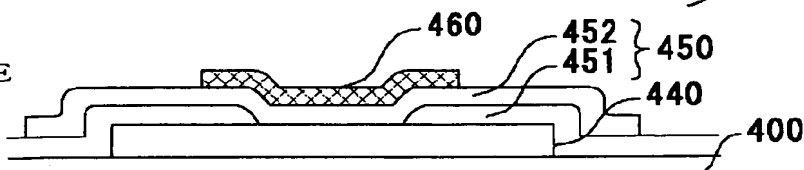


Fig. 8F

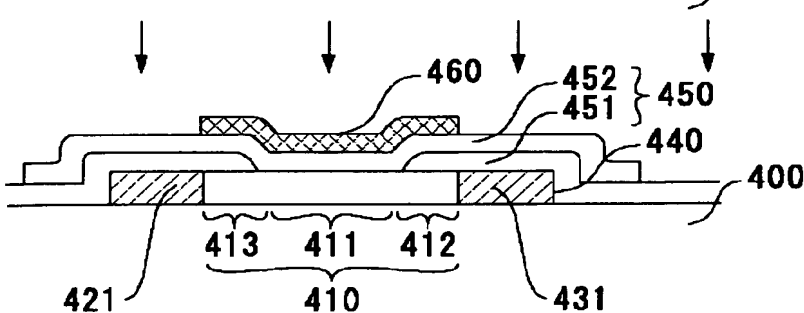


Fig. 8G

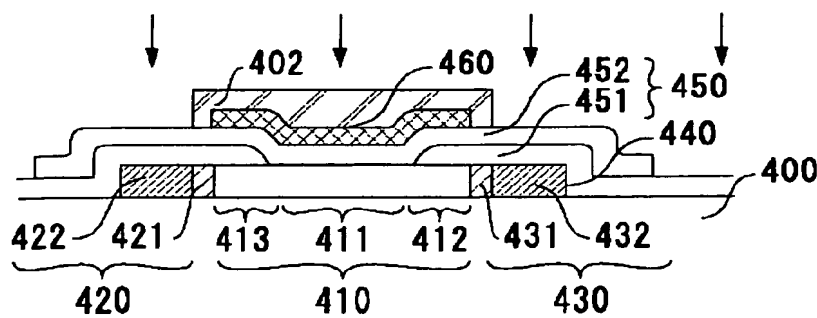


Fig. 9A

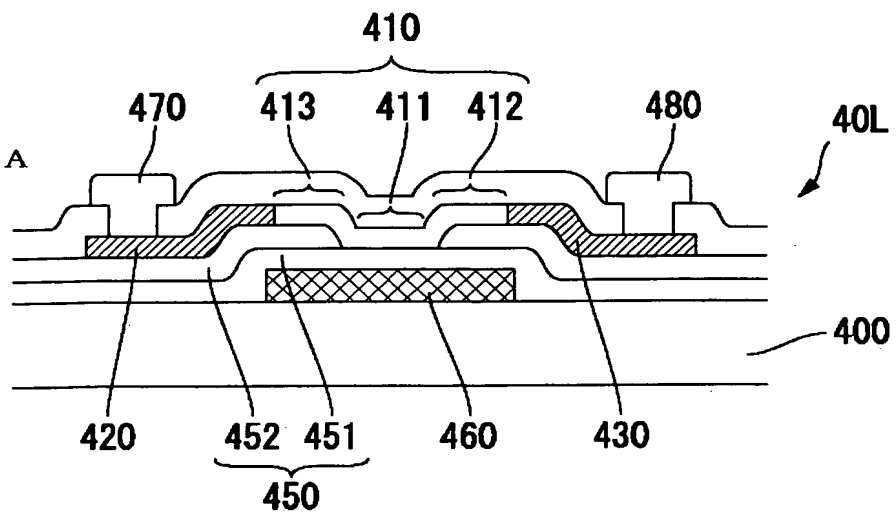


Fig. 9B

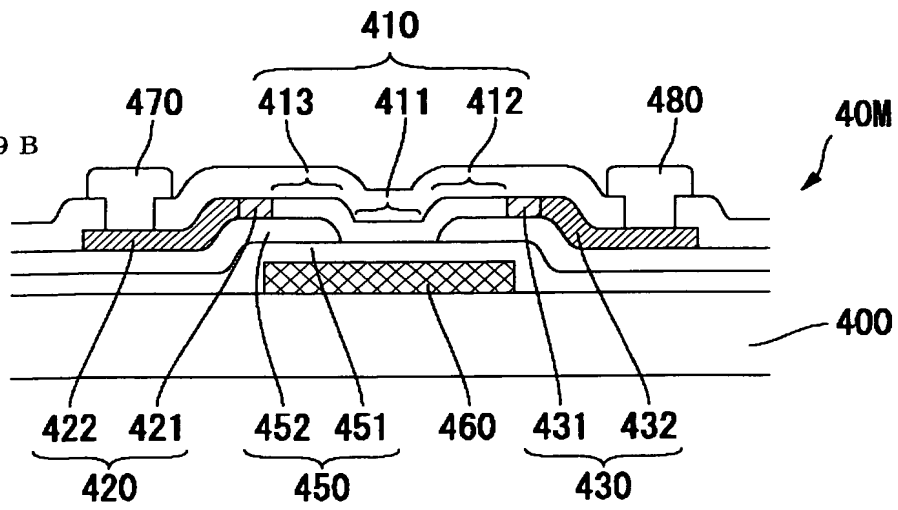


Fig. 10

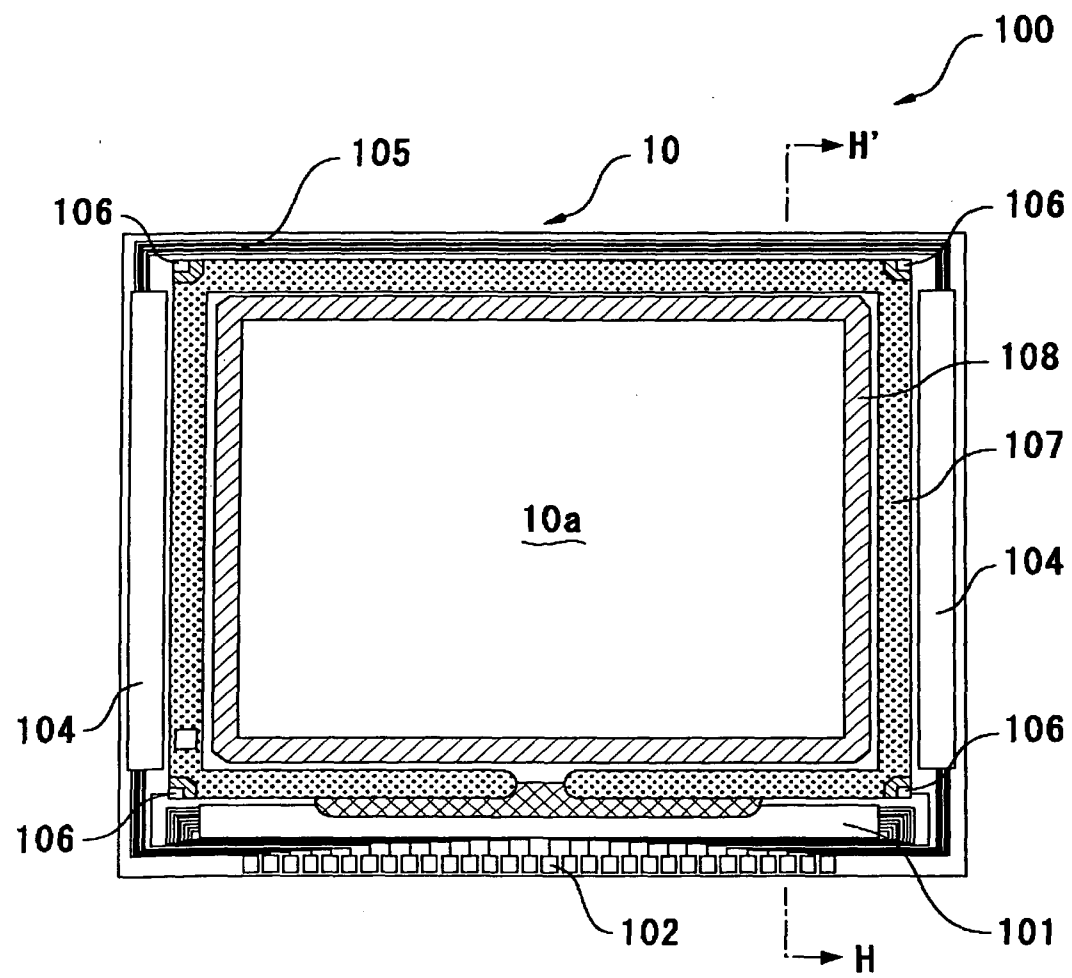


Fig. 11

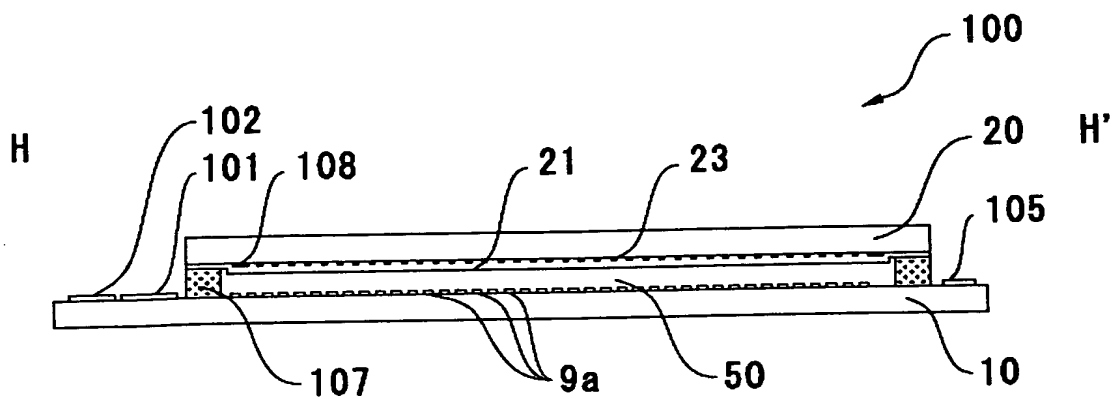


Fig. 12

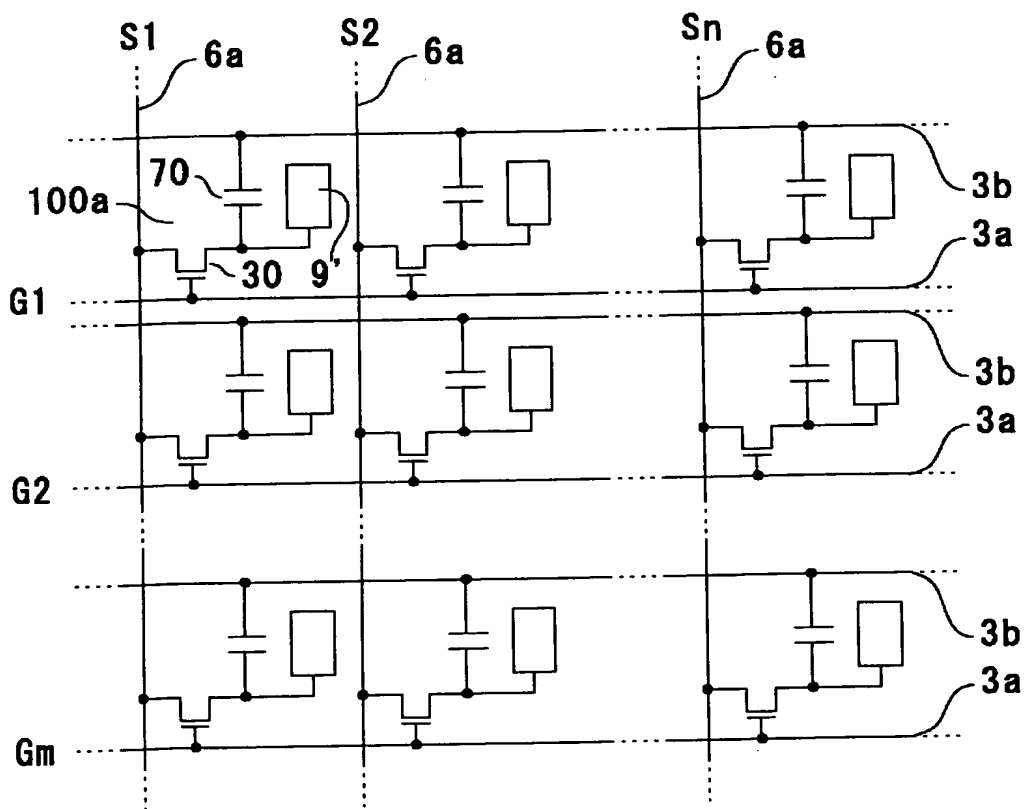


Fig. 13

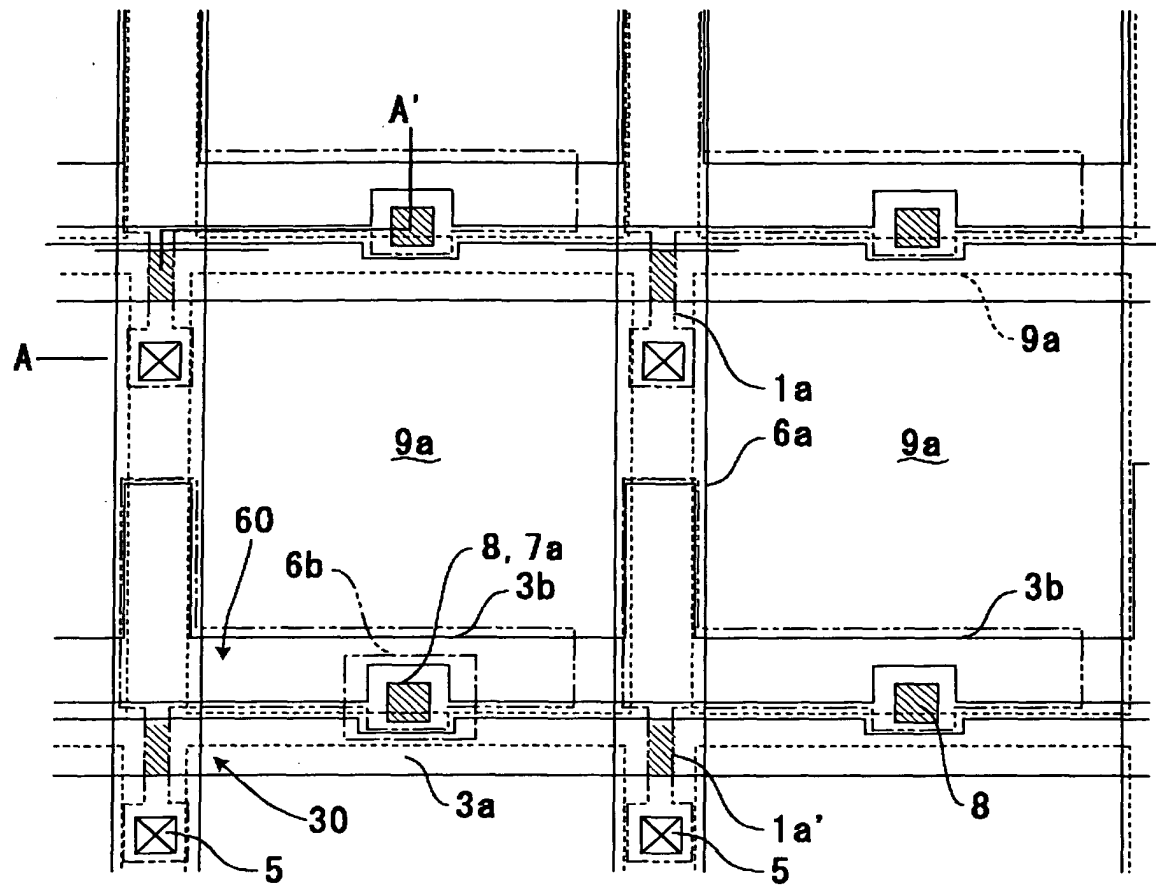


Fig. 14

IMAGE DISPLAY REGION 10a

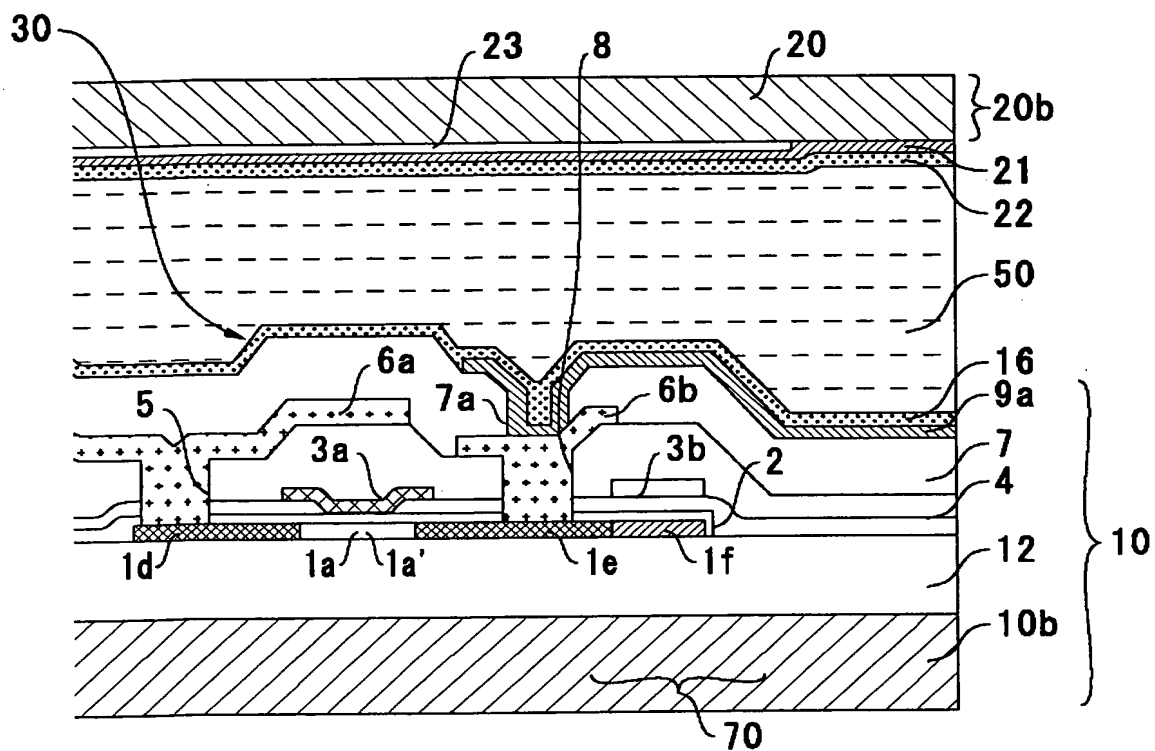


Fig. 15

PERIPHERAL CIRCUIT REGION

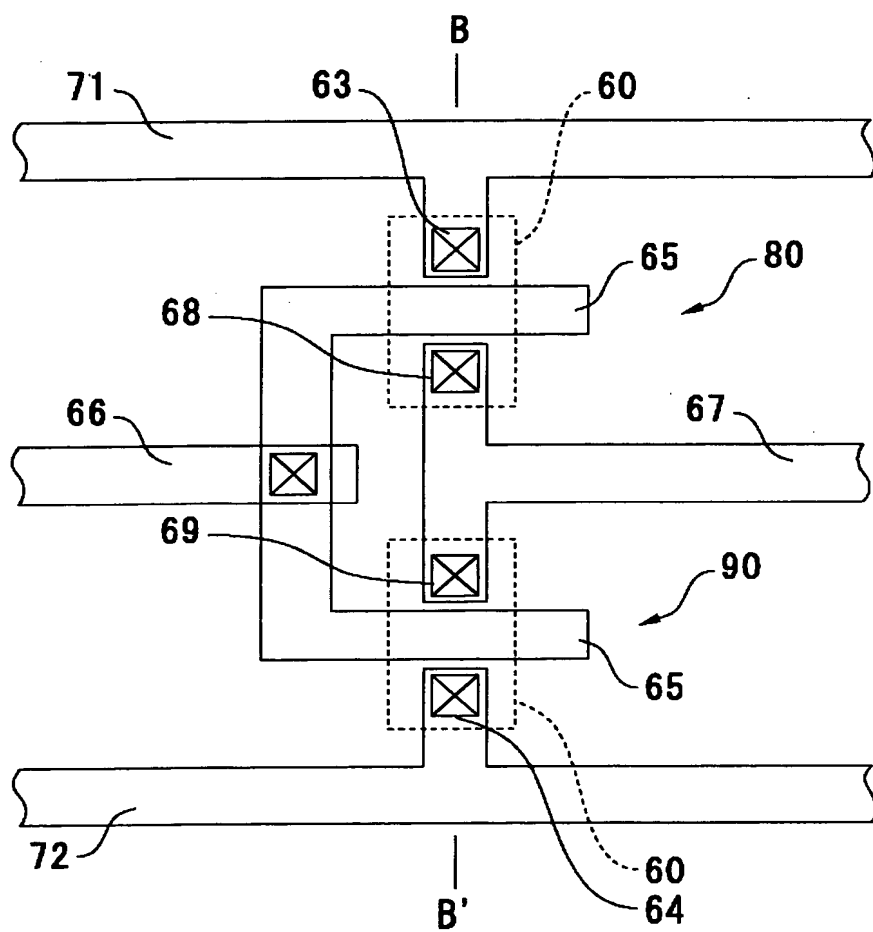


Fig. 16

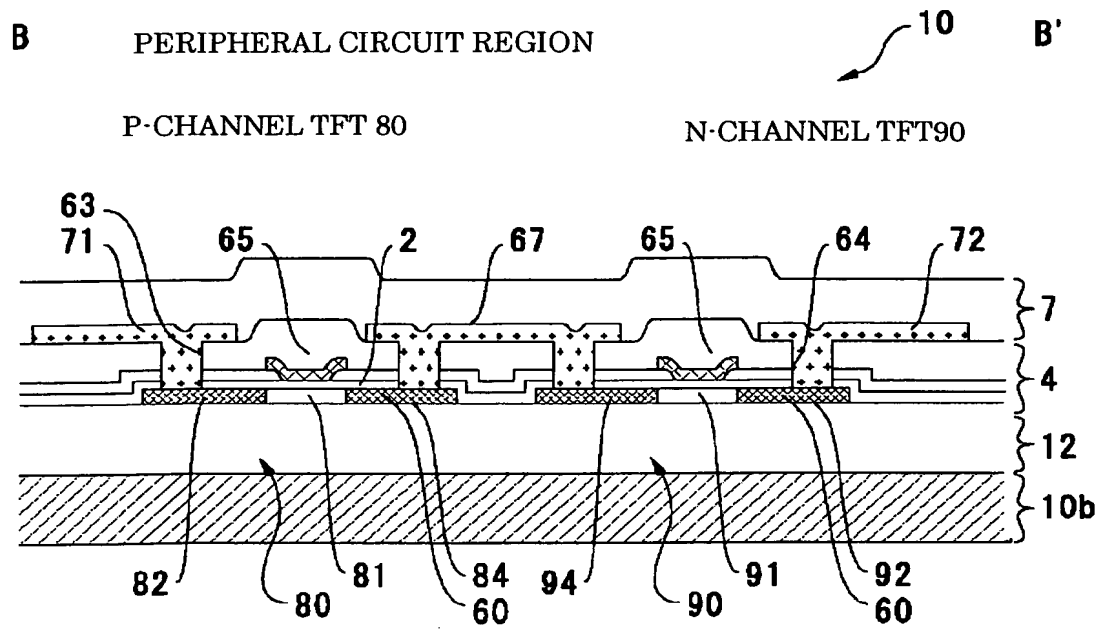


Fig. 17

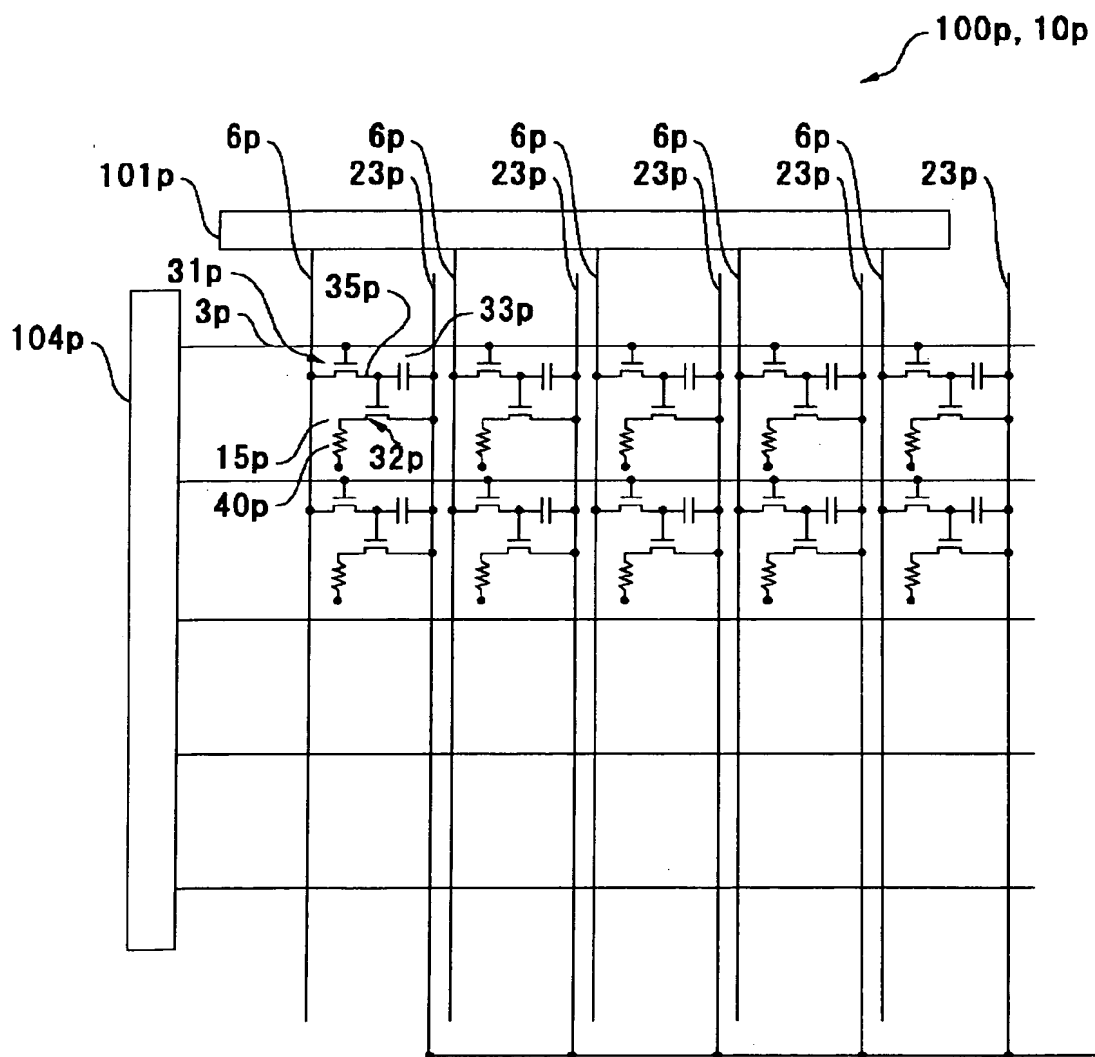


Fig. 18A

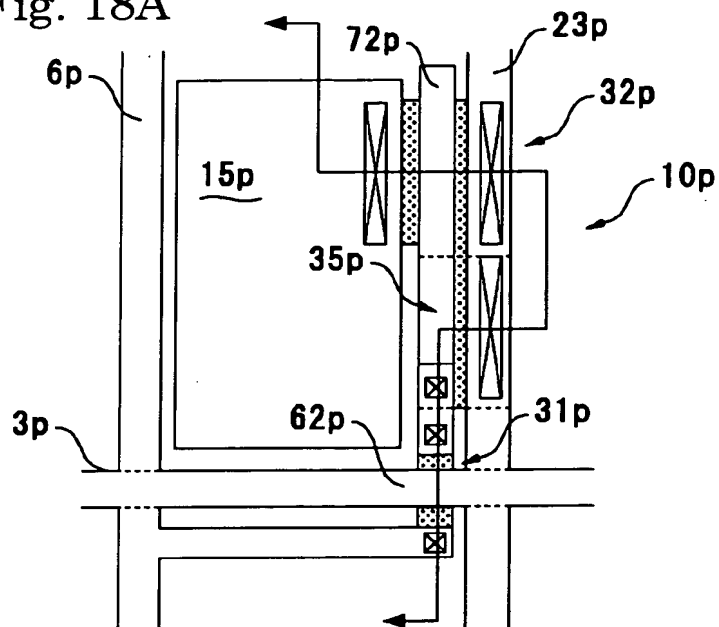


Fig. 18B

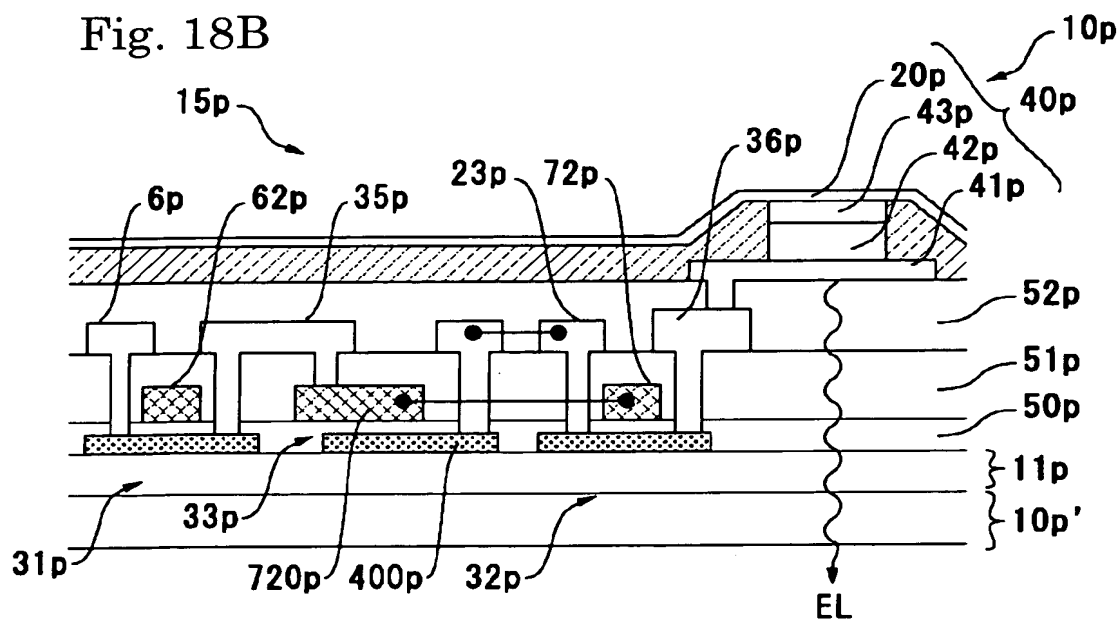


Fig. 19

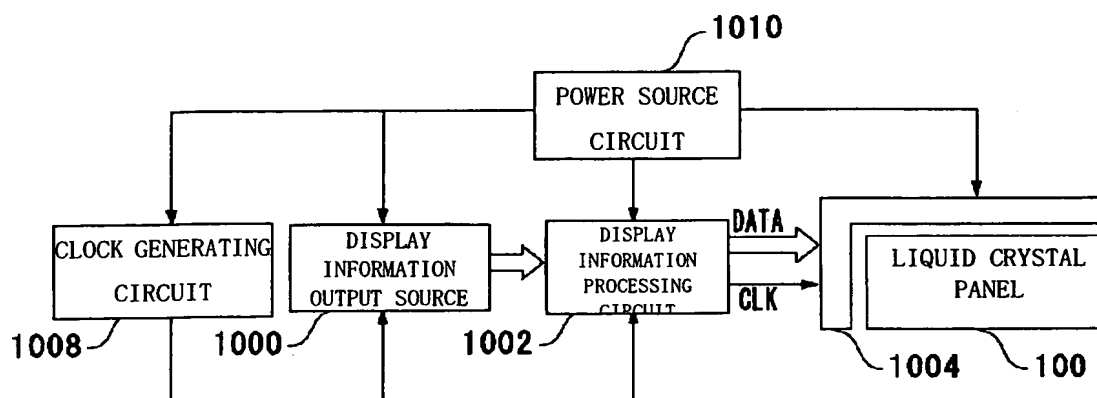


FIG. 20A

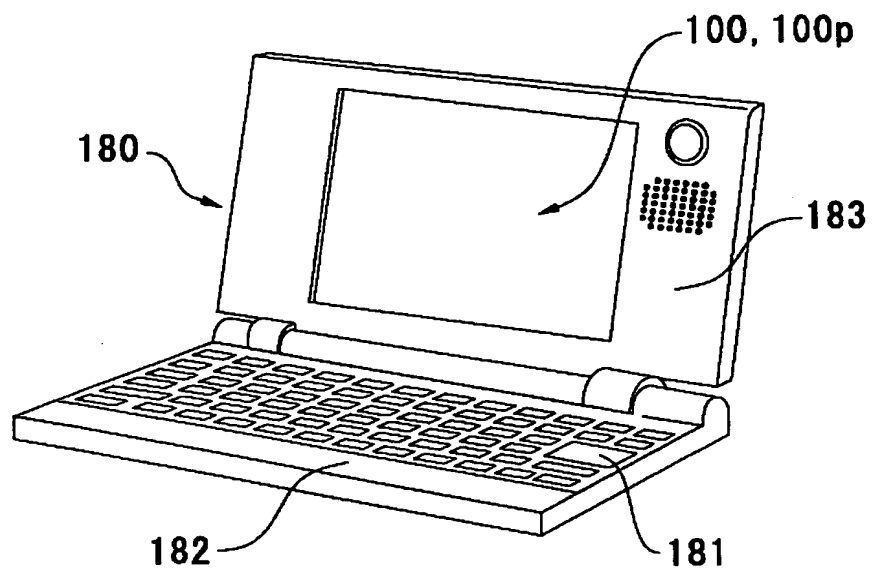


FIG. 20B

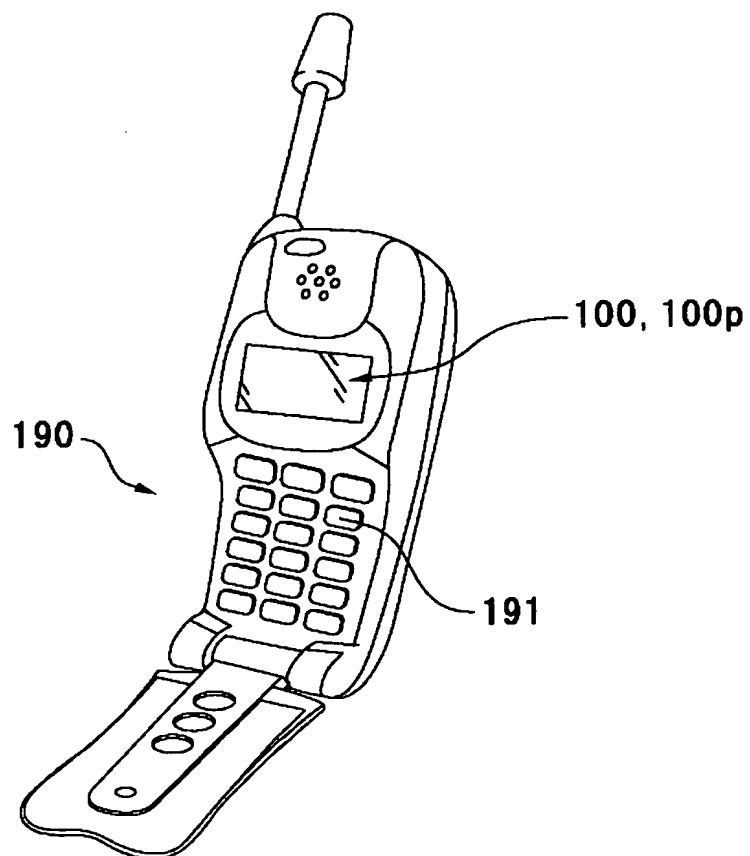


Fig. 21A

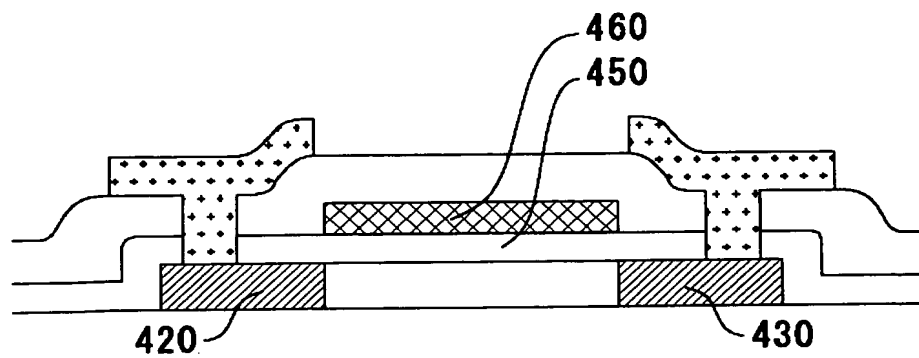


Fig. 21B

